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## FOR QUERIES, PLEASE CONTACT:

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Organizing Secretary

**Mr. JAWAID ALAM-09650106759,**  
**Mr. JAVED ASHRAF-09810415196**

Conveners

E-mail : [ece@afset.org](mailto:ece@afset.org), Website: [www.afset.org](http://www.afset.org)

## NATIONAL CONFERENCE

On

## Advances in VLSI, Embedded & Communication (AVEC-2010)



Tuesday, 10<sup>TH</sup> AUGUST, 2010



Organized by

Deptt. of Electronics &  
Communication Engg.



**AL - FALAH SCHOOL OF  
ENGINEERING & TECHNOLOGY**  
(An Autonomous Institute)

Dhauj, Faridabad, Haryana

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## ABOUT THE COLLEGE

Al Falah School of Engineering & Technology (AFSET) is the first Autonomous Engineering College in Haryana, established in 1997 by the munificence of philanthropists and well-wishers in the minority community. The college is run by Al Falah Charitable Trust, consisting of eminent industrialists and academicians led by the pioneer Mr. Jawad Ahmad Siddiqui. It is an ISO 9001:2000 certified with NBA Accredited programmes, approved by the A.I.C.T.E., New Delhi, & DTE Government of Haryana and affiliated to Maharishi Dayanand University, Rohtak. Al Falah Charitable Trust runs tow more institutions namely Brown Hills College of Engineering & Technology & Al Falah School of Education & Training in the same campus, spread over 56 acres of land.

## COURSES OFFERED BY AFSET

### B.Tech.Programmes in Engineering (total 6)

- Electronics & Communication Engineering
- Mechanical & Automation Engineering
- Electrical & Electronics Engineering
- Computer Science & Engineering
- Mechanical Engineering
- Civil Engineering

### M.Tech. Programmes in Engineering (total 6)

- Electronics & Communication Engineering
- Manufacturing & Automation Engineering
- Computer Science & Engineering
- Machine Design
- Power System
- VLSI Design

### Department of Management Studies

- Master of Business Administration
- Bachelor of Business Administration

### PhD Programmes in Applied Sciences, Management & Engineering

## ABOUT THE ECE DEPARTMENT

The Department of Electronics and Communication Engineering has well qualified and experienced Faculty with both Academic and Research Experience. ECE Department has Appx.all latest versions of softwares like Matlab, Tanner Tools, Multisim, Keil, Flash Magic, Simili, Lab View along with kits like FPGA, CPLD, BLACK FIN, 8051, PIC etc for our students to meet market demand.

## OBJECTIVE OF THE CONFERENCE:

To present an overview of the recent technological developments with ongoing research in the field of VLSI, Embedded & Communication Technologies and provide the platform for interaction among academicians, researchers, industrialists & innovative students.

## TOPICS FOR CONFERENCE

Papers are invited on topics related, but not limited, to the following areas (Poster Presentation will also be accepted )

## VLSI/ EMBEDDED

VLSI Technologies, Digital System Design, Device Modeling and Simulation, CAD VLSI Design, Analog /Digital VLSI Design, CMOS Mixed Signal Circuit Design, MEMS & NEMS, Nanotechnology, Algo for VLSI Design Automation, VLSI Test & Testability, ASIC Design, Hardware-Software Co-Design, Low Power Design & VLSI Physical Synthesis, Design Methodology for CLPDs and FPGAs etc.  
Embedded System Design using 8051/PIC, ARM Development with Open Source Tools, Hardware-Firmware Interface Design, Multicore Systems Design, Controller Area Networking etc.

## COMMUNICATION

Optical communications , Antennas and wave propagation, Satellite/Mobile/wireless communications, Image processing, ADSP, DSP algo and architectures , pattern recognition, and classification, Coding theory,Neural Networks & Fuzzy Logics,,VLSI for communications,Sensor networks , Ad hoc networks ,Wireless and wireline networks , Security factors etc.

## KEY DATES

Submission of full length paper : 02-08-2010  
Acceptance Intimation : 04-08-2010  
Advance Registration : 06-08-2010

## REGISTRATION FEE:

Registration Fee is **Rs.1000/-** for all categories of delegates.

**Note:** - Registration Fee includes participation in all technical sessions, refreshment, lunch and bus facility for all college routes from Delhi, Palwal, Faridabad.

The registration fee should reach conference desk on (or) before the due date. The registration fee is to be paid through Demand Draft drawn in the favor of "Al Falah School of Engineering & Technology", payable at New Delhi.

The complete registration Form should be sent by speed post at the following Address.

### Mr. M. Nizamuddin

Organizing Secretary AVEC-2010  
274-A,Al Falah House, Jamia Nagar,Okhla,New Delhi 110025

or

Scanned copy of Demand Draft can be mailed along with registration form at [ece@afset.org](mailto:ece@afset.org)

## LOCATION OF COLLEGE

**Al Falah School of Engineering & Technology** (AFSET) is located at Village Dhauj, District Faridabad, 20 km from Delhi (Badarpur Boarder).Nearest Airport is New Delhi about 35 km from AFSET.

**Note:**-Accommodation for Delegates can be arranged in nearby hotels on request at their expense.

## REGISTRATION FORM

(Xerox copy may be used)

Name.....  
Designation.....  
Department.....  
Organization.....  
Title of the Paper.....  
Area of the Paper.....

### Mailing Address

.....  
.....  
.....  
Telephone No.....  
Mobile No.....  
E-mail Id.....

### Details of Demand Draft

Amount: Rs.....  
DD No:.....  
Date:.....  
Bank Name .....

### Signature of the Delegate .....

The registration fee is to be paid through Demand Draft drawn in favor of "**Al Falah School of Engineering & Technology**", payable at New Delhi.

**Note:** Proceedings of the conference will be provided in a soft copy on CD

## REGISTRATION PROCEDURE

Prospective authors including Faculty, Industry Representatives, Research Scholars and PG Students are requested to send soft copy of full length paper in IEEE double column format by Email only at [ece@afset.org](mailto:ece@afset.org). The selected papers will be notified to the candidate for registration. All submission must include Title, Authors and Affiliations, Full address for Correspondence including Phone.